

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 12-16-2003  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Adaptive Transition Density Data  
Triggered PLL (Phase Locked Loop)  
Attorney Docket Number:: X-1056 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 08  
Small Entity?:: No  
Petition included?:: No

### APPLICANT INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Brian  
Middle Name:: T.  
Family Name:: Brunn  
Street:: 2803 Lenwood Court  
City:: Austin  
State or Province:: Texas  
Postal or Zip Code:: 78738

### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

### REPRESENTATIVE INFORMATION

Repres ntative Customer Numb r::	24309	
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**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124